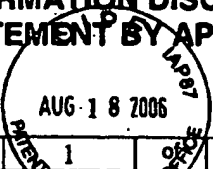


Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT 		Application No.	09/748,098
		Filing Date	December 21, 2000
		First Named Inventor	Nicholas J. Kelsey
		Art Unit	2183
		Examiner Name	David J. Huisman
Sheet	1 of 1	Attorney Docket Number	20880-05093

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Examiner Initials*	Cite No. ¹	Document No. Number - Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
DJH	A1	US-6,163,839	12-19-2000	Janik et al.
DJH	A2	US-6,061,710	05-19-2000	Eickemeyer et al.
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OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		
DJH	C1	8-bit AVR® Microcontroller with 128 K Bytes In-System Programmable Flash - Atmega103/103L - Preliminary; ATMEL; ©Atmel Corporation 2000; pp. 1-126.		
DJH	C2	LINDSAY, S.K. et al., "On the Performance of a Multi-Threaded RISC Architecture," Electrical and Computer Engineering, September 1993, pp. 369-372, Vol. 1.		
DJH	C3	Notification of Transmittal of the International Search Report, PCT/US00/35242, April 27, 2001, 4 pages.		
DJH	C4	Supplementary European Search Report, EP 00989474, November 23, 2004, 3 pages.		

Examiner Signature	/David Huisman/	Date Considered	10/23/2006
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

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